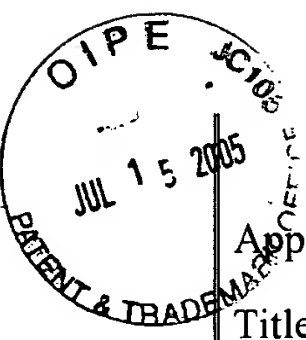


2123 <sup>2123</sup>



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Wu et al.  
Title: Hot Carrier Circuit Reliability Stimulation  
Application No.: 09/832,933 Filing Date: April 11, 2001  
Examiner: Mary C. Hogan Group Art Unit: 2123  
Docket No.: BTAT.002US1 Conf. No.: 5253

Certificate of Mailing Under 37 CFR 1.8

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Gillen Bower  
Signature

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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicant(s) call(s) the documents listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application.

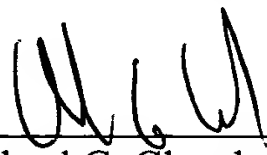
According to 37 C.F.R. 1.98(2)(ii), copies of the U.S. Patent Documents and U.S. Published Patent Application Document are not required and are therefore not enclosed. Copies of the listed Other Art Documents are enclosed.

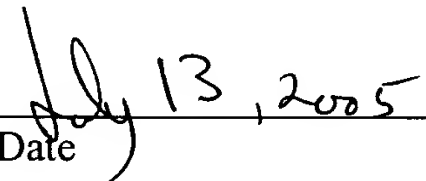
Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a representation that a search has been made (other than as indicated by any cited document), or

(3) an admission that the cited information is, or is considered to be, material to patentability as defined in § 1.56(b).

This information disclosure statement is submitted under 37 C.F.R. § 1.97(b) and consequently no fee should be required. The Commissioner is authorized, however, to charge any fee that may be required, or to credit any overpayment, against Deposit Account No. 502664. This form is being submitted in duplicate.

Respectfully submitted,

  
\_\_\_\_\_  
Michael G. Cleveland  
Reg. No. 46,030

  
\_\_\_\_\_  
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U.S. Department of Commerce, Patent and Trademark	Atty. Docket No.	Application No.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	BTAT.002US1	09/832,933
	Applicant(s)	Conf. No.
(Use several sheets if necessary)	Wu et al.	5253
(Form PTO-1449)	Filing Date	Art Group
	April 11, 2001	2123

## U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	1	6,530,064	3/4/03	Vasanth et al.			
	2	5,615,377	3/25/77	Shimizu et al.			
	3	6,795,802	9/21/04	Yonezawa et al.			

## U.S. Published Patent Application Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	4	2003/0158713A1	8/21/03	Akimoto et al.			

## Foreign Patent Documents

							Translation	
		Document	Date	Country	Class	Subclass	Yes	No

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

5	Li et al., "A Probabilistic Timing Approach to Hot-Carrier Effect Estimation," <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , Vol. 13, No. 10, October 1994, pp. 1223-1234.
6	IsSpice4 User's Guide, <i>Intusoft</i> , 1988-1996, pp. 196-216.
7	Kawakami et al., "Gate-Level Aged Timing Simulation Methodology for Hot-Carrier Reliability Assurance," <i>Proceedings of the ASP-DAC 2000, Asia and South Pacific</i> , Jan. 25-28, 2000, pp. 289-294.
8	Ye et al., "Developing Aged SPICE Model for Hot Carrier Reliability Simulation," <i>Integrated Reliability Workshop Final Report</i> , 2000, IEEE International, Oct. 23-26, 2000, pp. 153-154.

Examiner

Date Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.